

Hybrid-Modulation-Based Control Technique for Reduction of Output Voltage Ripples in Frequency-Modulated Switch-Mode Power Converters

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ABSTRACT

In this paper a novel control technique for switching-frequency-modulated switch-mode power converters (SMPC) operating in discontinuous conduction mode is proposed. The use of the technique leads to significant reduction in peak-to-peak output voltage and peak currents increased due to straightforward application of switching frequency modulation (SFM). The technique is based on hybrid modulation scheme in which both switching frequency and duty ratio are modulated simultaneously by the same modulation signal. Theoretical analysis and experimental verification of the proposed technique are presented in details. Both computer simulations and experiments show that switching-frequency-modulated SMPC with the proposed control technique in comparison to SMPC without SFM has appreciably lower conducted electromagnetic emissions, at the cost of slightly increased peak-to-peak output voltage and peak currents.

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1. INTRODUCTION

With an ever increasing number of electrical and electronic devices (e.g. personal computers, TV sets, vacuum cleaners, etc.), efficient electric power conversion has become a hot topic of research. DC/DC or AC/DC electric power converters which are used for efficient electric power conversion in electronic equipment are dominantly switch-mode ones. Switch-mode power converters (SMPC) have distinct advantages over other types of power converters (e.g. linear power supplies) – mainly higher efficiency, lower size and weight [1], [2]. However owing to rapidly switching semiconductor devices with high voltage and current change rates they are major sources of electromagnetic interference (EMI) both conducted and radiated [3].

Conducted EMI is usually reduced by using passive or active EMI filters, interleaving, soft-switching techniques, spread spectrum techniques, etc. [4] – [11]. Since every conducted EMI suppression technique has its own advantages and disadvantages, often they are used in combination with other EMI reduction approaches. For example, it is often not enough to use spread spectrum approach alone to reduce conducted EMI significantly and keep it within allowable limits [10]. Spread spectrum technique is often used in combination with passive EMI filtering. In this case passive EMI filter size and weight can be reduced noticeably [12].

This paper deals with the spread spectrum technique based on periodic switching frequency modulation (SFM) applied to SMPC to reduce conducted EMI. In fact, the technique has gained considerable attention during past two decades because of its easiness of implementation and ability not to increase

weight, size and cost of SMPC. However the main problem with the use of this technique in conventional DC/DC or AC/DC SMPC is that peak-to-peak output voltage and power stage peak currents (e.g. power inductor or power MOSFET currents) can increase significantly [13] – [16]. Increase in peak-to-peak output voltage of SMPC can be very harmful to electronic equipment powered by the SMPC, but increase in peak currents of power magnetic components can lead to increase in losses and even to saturation of a magnetic core of transformers or inductors.

In [13] – [17] the effect of SFM on peak-to-peak output voltage and power stage peak currents of DC/DC SMPC operating in continuous conduction mode (CCM) and discontinuous conduction mode (DCM) was studied in details and some recommendations for proper design of DC/DC SMPC were proposed. However, the recommendations are not sufficiently useful to get low peak-to-peak output voltage and power stage peak currents, especially for switching-frequency-modulated SMPC operating in DCM. This is why in this paper a novel control technique for significant reduction of peak-to-peak output voltage and power stage peak currents in switching-frequency-modulated SMPC is proposed. The control technique is based on hybrid modulation scheme in which both switching frequency and duty cycle are modulated by the same modulation signal. Along with appreciable conducted EMI reduction, the technique also decreases significantly peak-to-peak output voltage and power stage peak currents increased due to the use of SFM. In order to show usefulness of the proposed technique a simple analog circuit based on analog multiplier will also be proposed and analyzed.

The proposed control technique can be applied to both classical single-switch non-isolated (e.g. buck, boost, buck-boost) and isolated (e.g. flyback, forward) SMPC topologies operating in DCM. The proposed method is not suitable for SMPC operating in CCM, because in this case variations of duty ratio can lead to large low-frequency ripples in SMPC output voltage.

The paper is organized as follows. In section 2 the effect of the proposed hybrid modulation scheme on DCM boost DC/DC SMPC peak-to-peak output voltage and power stage peak currents will be studied analytically and by computer simulations. In section 3 thorough experimental verification of the proposed technique will be performed. Finally, conclusions are given in Section 4.

2. THEORETICAL ANALYSIS OF THE PROPOSED CONTROL TECHNIQUE

Based on the analysis presented in our papers [15] – [17] and after analyzing expressions of output ripple voltage for traditional switching-frequency-modulated single-switch SMPC (e.g. buck, boost, buck-boost, forward and flyback) operating in DCM it can be shown that general expression of peak-to-peak output voltage when SFM is enabled is as follows:

$$V_{FMpp} = \max\{v_{LF}(t) + v_{envHF}(t)\} - \min\{v_{LF}(t) - v_{envHF}(t)\} \quad (1)$$

where $v_{LF}(t)$ is low-frequency (LF) ripple of output voltage caused by SFM; $v_{envHF}(t)$ is high-frequency (HF) ripple envelope. The LF ripples in DCM are due to the fact that averaged inductor, diode and transistor switch currents are switching-frequency-dependant [17]. The envelope of HF ripple in unmodulated SMPC is constant, but in switching-frequency-modulated SMPC it is variable (because switching frequency changes in time). Both $v_{LF}(t)$ and $v_{envHF}(t)$ are functions of instantaneous switching frequency $f_{sw}(t)$ and instantaneous duty ratio $d(t)$:

$$v_{LF}(t) = \psi_1 [d(t), f_{sw}(t)] \quad (2)$$

$$v_{envHF}(t) = \psi_2 [d(t), f_{sw}(t)], \quad (3)$$

Where

$$d(t) = D + \tilde{d}(t), \quad (4)$$

$$f_{sw}(t) = f_{sw0} + \Delta f_{sw} m(t), \quad (5)$$

where D is average duty ratio; $\tilde{d}(t)$ is variable component of duty ratio; Δf_{sw} is switching frequency deviation; $m(t)$ is modulation waveform with unitary amplitude; f_{sw0} is central switching frequency. Note further in the analysis it will be assumed that $m(t)$ is periodic and f_m is modulation frequency.

For frequencies f_m lower than open loop gain crossover frequency (f_{cross}) feedback loop changes duty ratio with the purpose to reduce the output voltage LF ripple. In order to simplify our analysis let's assume that $f_m > f_{cross}$. In this case $d(t)$ is almost constant and equals D approximately.

For quite large Δf_{sw} , V_{FMpp} and magnetic components peak currents can increase significantly in DCM in comparison to unmodulated case [16], [17]. Therefore it is important to propose a control technique to reduce peak-to-peak output voltage and peak currents increased due to straightforward application of SFM.

Now we are going to show some theoretical analysis in order for a reader to better understand the idea. Assuming that $f_m > f_{cross}$, (2) and (3) can be overwritten as follows:

$$v_{LF}(t) = \psi_1 [D + \delta(t), f_{sw}(t)] \quad (6)$$

$$v_{envHF}(t) = \psi_2 [D + \delta(t), f_{sw}(t)] \quad (7)$$

where $\delta(t)$ is time-dependant component of the duty ratio which should be generated and introduced into a non-inverting input of the control block comparator so that

$$V_{FMpp} = V_{pp} \quad (8)$$

where V_{pp} is peak-to-peak output ripple voltage of unmodulated (without SFM) SMPC. In order to derive $\delta(t)$, expressions for $v_{LF}(t)$ and $v_{envHF}(t)$ should be known and equation (8) with respect to $\delta(t)$ then should be solved.

For switching frequencies up to several hundreds of kHz electrolytic capacitors are often used at outputs of SMPC. Since for electrolytic capacitors at frequencies > 50 kHz usually equivalent series resistance (ESR) is much higher than capacitive reactance, then in order to simplify our analysis it will be assumed that HF ripples are due to the ESR only.

It can be proved that for single-switch SMPC (e.g. buck, boost, buck-boost, forward and flyback) with typical output electrolytic capacitor general expression for HF ripple is as follows:

$$v_{envHF}(t) = K \frac{d(t)}{2f_{sw}(t)} \quad (9)$$

where K is SMPC-topology-dependant coefficient, which e.g. for boost SMPC is as follows [17]:

$$K = \frac{r_c V_{in}}{L} \quad (10)$$

where V_{in} is input DC voltage; L is the power inductor's inductance; r_c is ESR of the output capacitor.

General expression for the LF ripple $v_{LF}(t)$ can be found after deriving expressions for AC component of averaged current ($\langle i_e \rangle$) before the output capacitor, applying the Laplace transform and deriving expression for the LF ripple in s-domain [17] as follows:

$$v_{LF}(s) = \tilde{i}_e(s) \frac{H_{out}(s)}{1 + T(s)} \quad (11)$$

where $H_{out}(s)$ is output voltage to i_e transfer ratio (in case of boost SMPC, it is output voltage to diode current transfer ratio); $T(s)$ is open loop gain; $\tilde{i}_e(s)$ is AC component of $\langle i_e \rangle$ in s-domain. In general case:

$$\langle i_e \rangle = F \frac{(d(t))^2}{f_{sw}(t)} \quad (12)$$

where F for e.g. boost SMPC [15], [17] is as follows:

$$F = \frac{V_{in}^2}{2L(V_{out} - V_{in})} \quad (13)$$

In time domain the LF ripple can be obtained by applying inverse Laplace transform of (11).

Despite the fact that optimum $\delta(t)$ for which V_{FMpp} is minimum can be found by solving equation (8) and using (1), (9) and (11), this is, firstly, very complicated task, and secondly, it would be very difficult to generate such a complex $\delta(t)$ in practice. This is why we are to derive simple approximate expression for

$\delta(t)$, which would give quite high reduction of V_{FMpp} increased due to the use of SFM. We can find the approximate expression by solving two equations separately: one for the variable duty ratio component $\delta_1(t)$ to get minimum peak-to-peak HF ripple voltage and another for the variable duty ratio component $\delta_2(t)$ to get zero peak-to-peak LF ripple voltage. To get minimum peak-to-peak HF ripple voltage the following equation should be solved:

$$K \frac{D + \delta_1(t)}{2(f_{sw0} + \Delta f_{sw} m(t))} = v_{env}(t) \tag{14}$$

where $v_{env}(t)$ is envelope of output voltage ripple for unmodulated SMPC:

$$v_{env}(t) = K \frac{D}{2f_{sw0}} \tag{15}$$

After solving (14), $\delta_1(t)$ can be found:

$$\delta_1(t) = A_1 D m(t), \tag{16}$$

where $A_1 = \Delta f_{sw} / f_{sw0}$.

In order to find $\delta_2(t)$ to get zero LF ripples, $\langle i_e \rangle$ of switching-frequency-modulated SMPC should be constant and equal to $\langle i_e \rangle$ for SMPC without SFM as follows:

$$F \frac{(D + \delta_2(t))^2}{f_{sw0} + \Delta f_{sw} m(t)} = F \frac{D^2}{f_{sw0}} \tag{17}$$

From this follows that in order to get zero LF ripple, $\delta_2(t)$ should be equal to $A_2 D m(t)$, where $A_2 = \Delta f_{sw} / (2f_{sw0})$.

It can be proved also that minimum peak current of magnetic components of switching-frequency-modulated SMPC can be achieved if $\delta(t)$ equals $A_1 D m(t)$, because the peak current for unmodulated SMPC is equal to ED / f_{sw0} (where $E = V_{in} / L$ for boost SMPC).

Based on the theoretical analysis shown above some conclusions can be drawn. Peak-to-peak output voltage and peak currents of power magnetic and semiconductor components increased due to the use of SFM can be reduced significantly using the hybrid modulation when switching frequency and duty ratio are modulated simultaneously by the same periodic modulation signal $m(t)$, because approximate $\delta(t)$ equals $ADm(t)$. Optimum value of the parameter A is somewhere between A_1 and A_2 . This value can be calculated by finding minimum of (1) analytically, numerically or using computer-simulation-based approach. If the latter is used then it is necessary to make many simulations for different values of A in range between A_2 and A_1 to find optimum A . If the LF ripples are much lower than the HF ripples, optimum A is equal to $\Delta f_{sw} / f_{sw0}$. If the LF ripples are much higher than the HF ripples, optimum A is equal to $\Delta f_{sw} / (2f_{sw0})$. Note that the conclusions are valid only when $f_m > f_{cross}$.

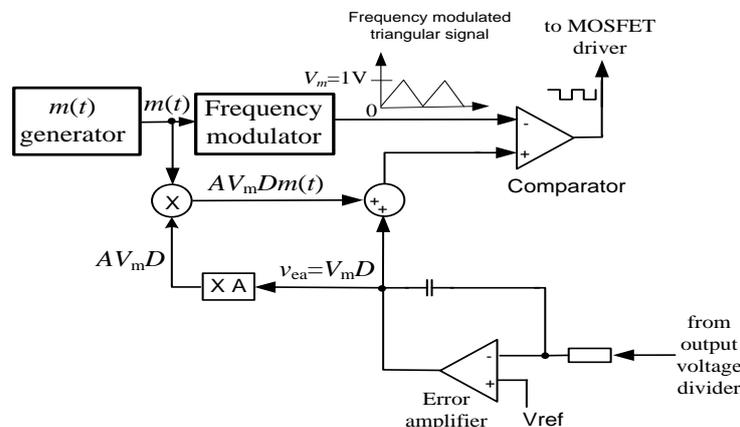


Figure 1. Switching-frequency-modulated SMPC control block with the proposed hybrid modulation

Block diagram of a switching-frequency-modulated SMPC control block with the proposed control technique based on the hybrid modulation is shown in Figure 1. Amplitude of switching frequency variations is equal to Δf_{sw} , but amplitude of duty ratio variations should be equal to A .

In order to show usefulness of the proposed control technique based on the hybrid modulation, a switching-frequency-modulated SMPC simulation model was created in PSIM software as shown in Figure 2. The model was initially simulated without SFM (when $f_{sw0}=100$ kHz; $V_{in}=12$ V; $R_{load}=100$ Ω ; $V_{out}=20$ V). Output voltage waveform, input current waveform and input current spectrum of unmodulated boost SMPC operating in DCM are depicted in Figure 3 a, Figure 4 a and Figure 5 a. Then the model was simulated with SFM enabled ($f_m = 10$ kHz, $\Delta f_{sw}=30$ kHz; $m(t)$ is sine). The results of the simulations are shown in Figure 3 b, Figure 4 b and Figure 5 b. As it can be seen SFM along with noticeable reduction of input current harmonic amplitudes leads to significant increase in peak-to-peak output ripple voltage and peak inductor current. As shown in Table 1 SFM leads to percentage increase in peak-to-peak output voltage as high as 42.4 % and percentage increase in peak inductor current as high as 41 %. However after applying the proposed control method based on hybrid modulation (with $A=0.3$), peak-to-peak output voltage and peak inductor current decreased significantly as shown in Figure 3 c and Figure 4 c. Along with high reduction of peak-to-peak output voltage and peak inductor current, reduction of input current harmonics amplitudes is almost the same as in case when conventional control scheme (in which switching frequency is modulated only) is used (see Figure 5 c). It should be noted that the technique proposed is less effective to reduce peak-to-peak output voltage when output capacitor (e.g. ceramic cap.) with capacitive reactance much higher than ESR is used.

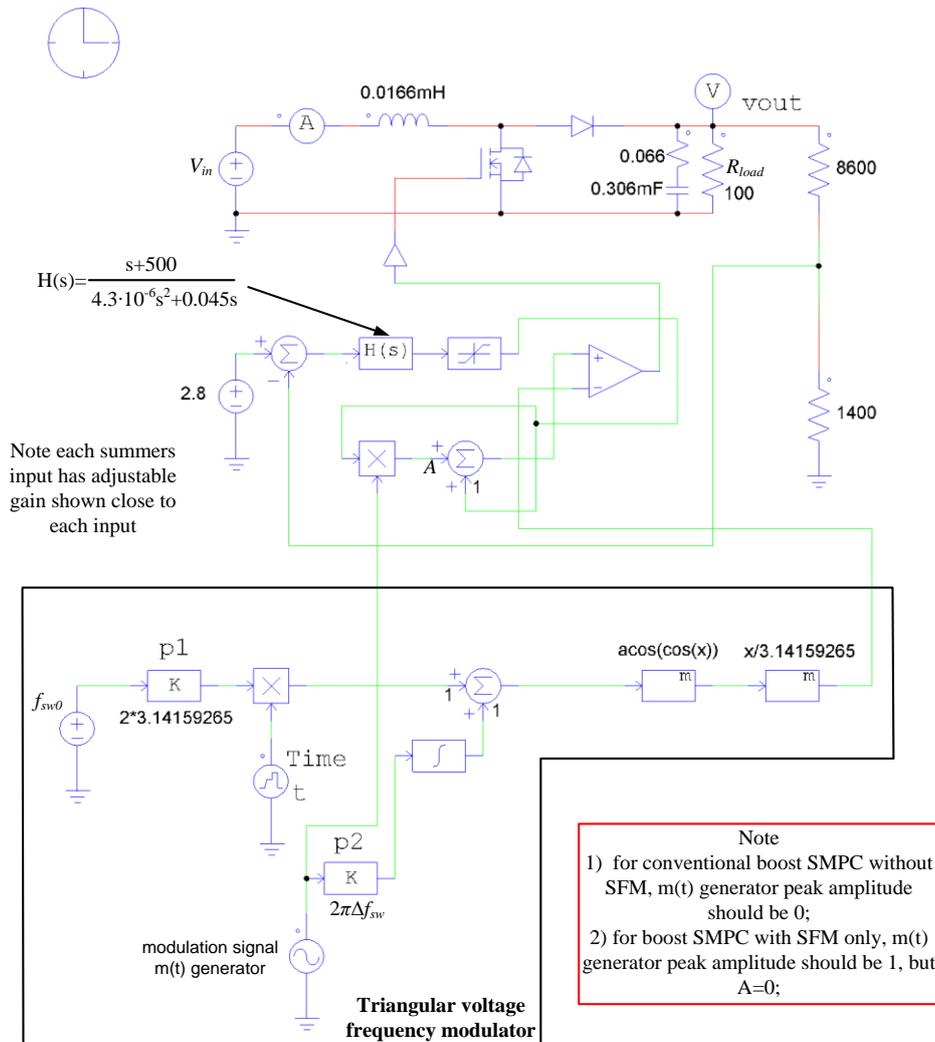


Figure 2. Boost SMPC simulation model created in PSIM. Note: the model can be used to simulate the boost SMPC without SFM, with SFM alone and with the proposed control technique based on hybrid modulation

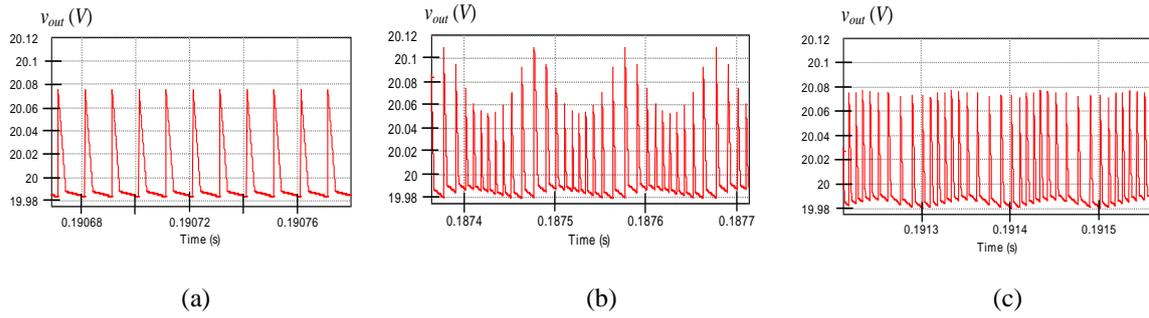


Figure 3. Simulated output voltage waveforms of boost SMPC in DCM: (a) for boost SMPC without SFM; (b) for boost SMPC with SFM only; (c) for boost SMPC with the hybrid modulation

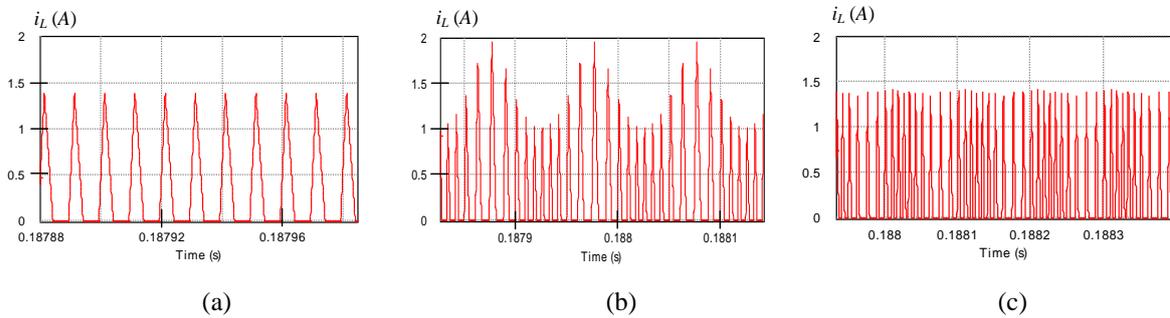


Figure 4. Simulated inductor current waveforms of boost SMPC in DCM: (a) for boost SMPC without SFM; (b) for boost SMPC with SFM only; (c) for boost SMPC with the hybrid modulation

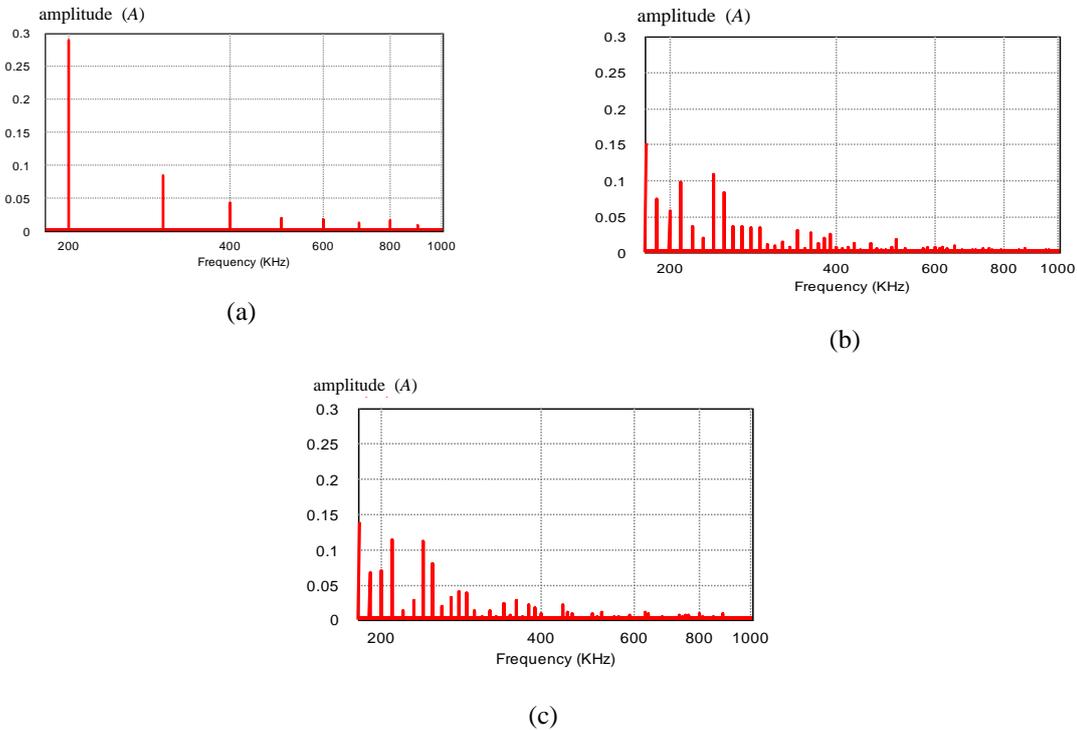


Figure 5. Simulated input current spectra of boost SMPC in DCM: (a) for boost SMPC without SFM; (b) for boost SMPC with SFM only; (c) for boost SMPC with the proposed control technique based on the hybrid

modulation

Table 1. Comparison of the simulation results ($f_m=10$ kHz, $f_{sw}=100$ kHz, $\Delta f_{sw}=30$ kHz; $m(t)$ is sine; $V_{in}=12V$)

	boost SMPC without SFM	boost SMPC with SFM	boost SMPC with the proposed control technique based on hybrid modulation
peak-to-peak output voltage, mV	91.3	130	97
percentage increase in peak-to-peak output voltage (in comparison to SMPC without SFM)	---	42.4 %	6.2 %
peak inductor current, A	1.39	1.96	1.42
percentage increase in peak inductor current (in comparison to SMPC without SFM)	---	41 %	2.1 %

3. EXPERIMENTAL VALIDATION OF THE PROPOSED CONTROL TECHNIQUE

3.1. Experimental setup

Detailed schematic diagram of an experimental prototype used to verify the usefulness of the proposed control technique is shown in Figure 6. The experimental boost SMPC can be tested without SFM, with conventional SFM and with hybrid modulation. Specifications of the experimental boost SMPC are shown in Table 2.

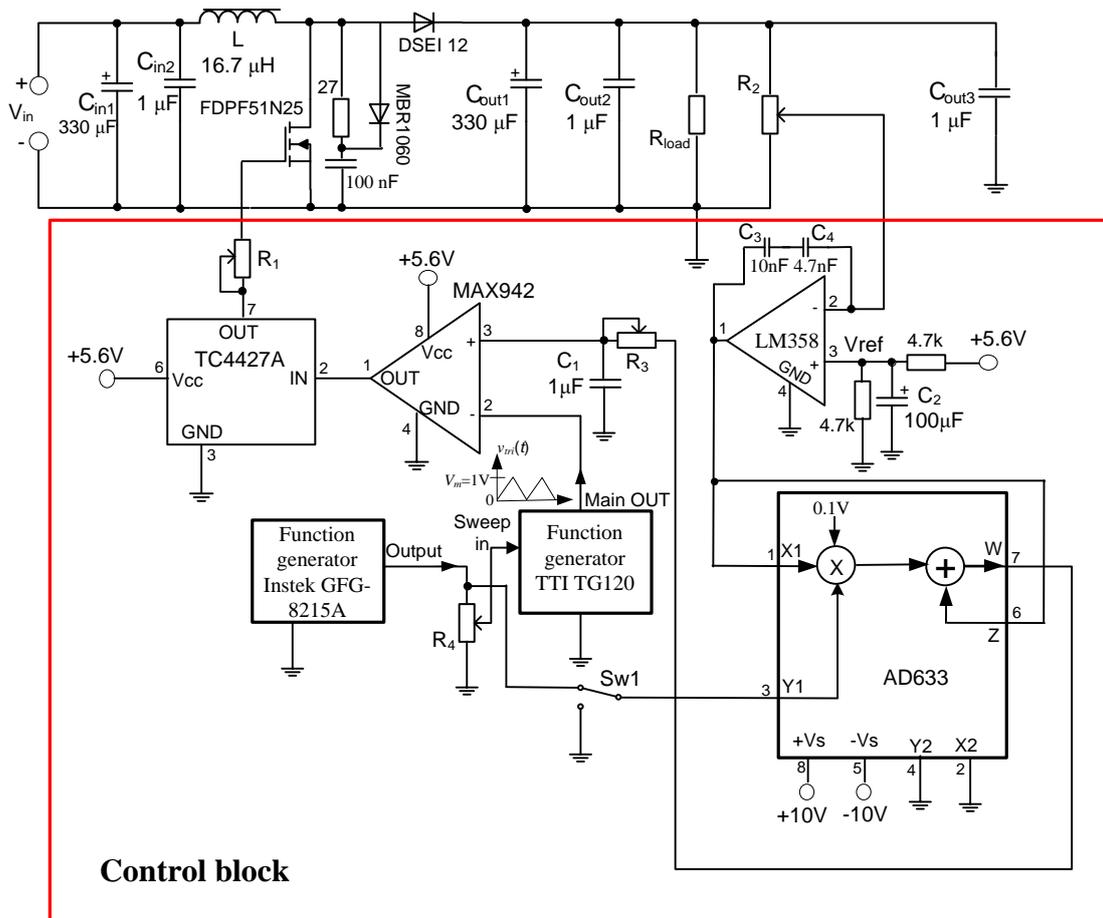


Figure 6. Detailed schematic diagram of the experimental boost SMPC

Table 2. Experimental prototype specifications

Input DC voltage	8 Vdc – 14 Vdc
Output DC voltage	20 Vdc
Nominal switching frequency, f_{sw0}	100 kHz
Type of control	Analog, voltage mode control
Output capacitors	2 ceramic capacitors (nominal capacitance of each capacitor is of 1 μ F); electrolytic capacitor (nominal capacitance 330 μ F; ESR \approx 66 m Ω)
Inductor	Ferrite E core with a non-magnetic gap; $L=16.7$ μ H

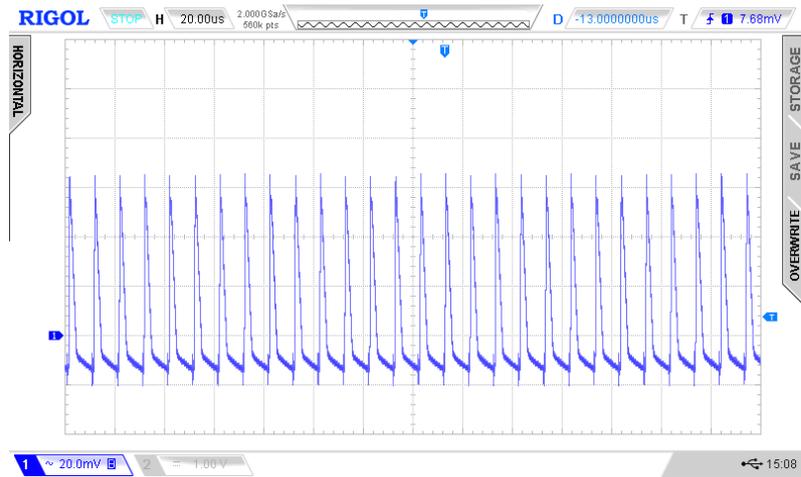
Four integrated circuits (ICs) and two function generators are used in the control block. The function generator Instek GFG-8215A is used as modulation signal generator. The function generator TTI TG120 is used as triangular voltage generator. The generator also has voltage controlled oscillator input (“sweep in”) for SFM. TC4427A is power MOSFET driver. Potentiometer R_1 is used to slow down the power MOSFET switching for the purpose to reduce the power MOSFET and output voltage spikes. Potentiometer R_2 is used to adjust desirable DC output voltage. Error amplifier is composed of IC LM358 and simple (Type 1) compensation network $R_2C_3C_4$. Low-pass filter R_3C_1 is used to reduce parasitic HF components in the comparator (IC MAX942) non-inverting pin input voltage. R_3 in our experiments was equal to 8.6 k Ω . C_2 is used to limit duty ratio at start. Resistor R_4 is used to regulate Δf_{sw} . IC AD633 is analog signal multiplier and summer which is used to implement the hybrid modulation. Voltage V_{ref} is a reference voltage. To disable SFM, the generator Instek should be turned off. If single pole double throw switch (Sw1) is in “up” position, then hybrid modulation is enabled, otherwise it is disabled (if Sw1 is in “down” position only switching frequency is modulated). It should be noted that the proposed control technique can also be implemented digitally using e.g. a microcontroller with suitable program code. This implementation is recommended.

3.2. Experimental results and discussion

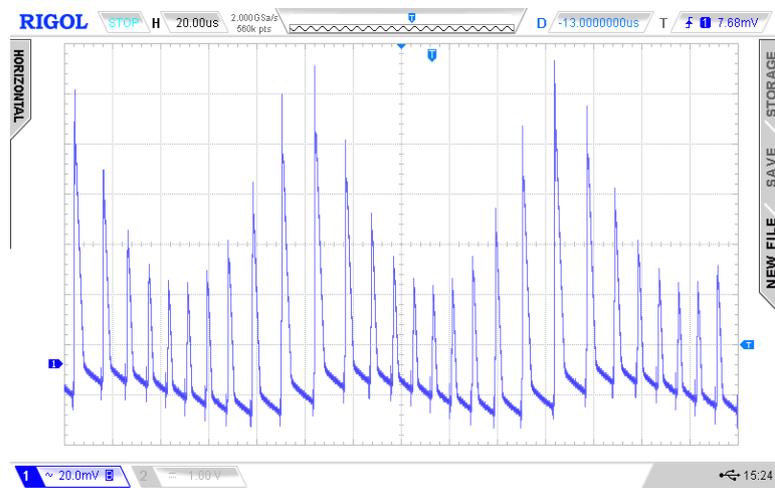
Initially the boost SMPC was examined when SFM was disabled. Output voltage waveform, input current waveform and conducted EMI levels of unmodulated boost SMPC (with $V_{in}=12$ V) operating in DCM are depicted in Figure 7 a, Figure 8 a and Figure 9 a. Then the experimental boost SMPC with SFM enabled was tested. The results (when $f_m = 9.5$ kHz, $f_{sw0}=100$ kHz, $\Delta f_{sw}=30$ kHz, $m(t)$ is sine, $V_{in}=12$ V, $R_{load}=100$ Ω , f_{cross} is of several kHz) of the measurements are shown in Figure 7 b, Figure 8 b and Figure 9 a. As it can be seen SFM along with noticeable reduction of conducted EMI levels leads to significant increase in peak-to-peak output ripple voltage and peak inductor current. As shown in Table 3 SFM leads to percentage increase in peak-to-peak output voltage as high as 70.1 % and percentage increase in peak inductor current as high as 42.1 %. However after applying the proposed control method based on hybrid modulation (with $A\approx 0.3$), peak-to-peak output voltage and peak inductor current decreased significantly as shown in Figure 7 c and Figure 8 c respectively. Along with high reduction of peak-to-peak output voltage and peak inductor current, reduction of the most dominant harmonic in conducted EMI spectrum is almost the same as in case when conventional control scheme (in which switching frequency is modulated only) is used (see Figure 9 b).

Table 3. Comparison of the experimental results ($f_m=9.5$ kHz, $f_{sw0}=100$ kHz, $\Delta f_{sw}=30$ kHz; $m(t)$ is sine; $V_{in}=12$ V; $R_{load}=100$ Ω ; f_{cross} is of several kHz)

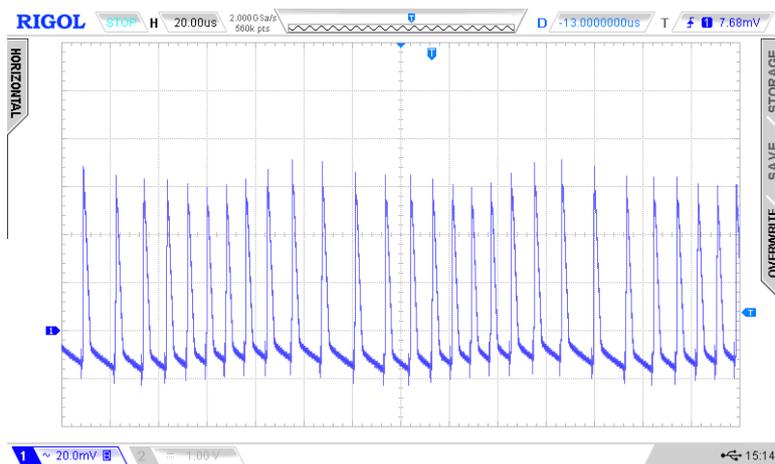
	boost SMPC without SFM	boost SMPC with SFM	boost SMPC with the proposed control technique based on hybrid modulation
peak-to-peak output voltage, mV	87	148	95
percentage increase in peak-to-peak output voltage (in comparison to SMPC without SFM)	---	70.1 %	9.2 %
peak inductor current, A	1.78	2.53	1.86
percentage increase in peak inductor current (in comparison to SMPC without SFM)	---	42.1 %	4.5 %



(a)

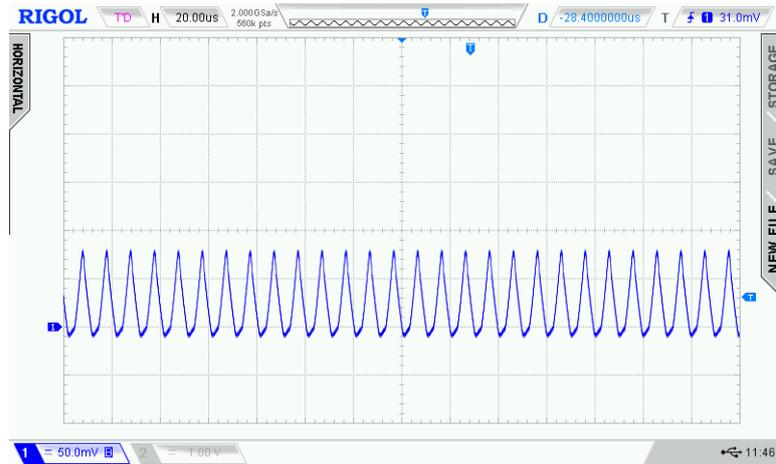


(b)

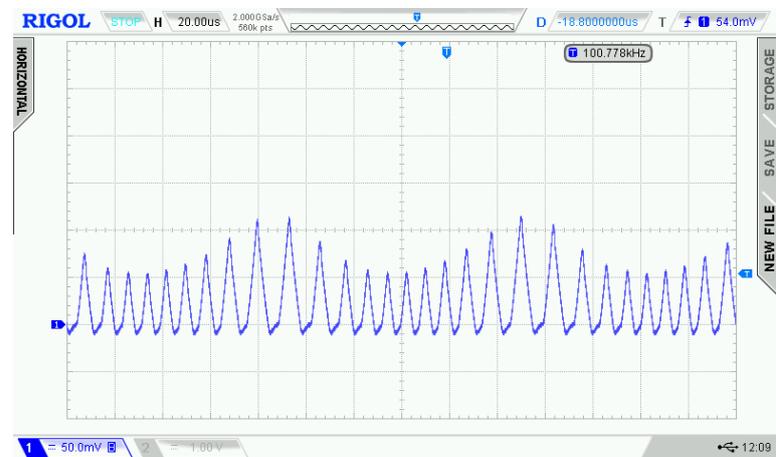


(c)

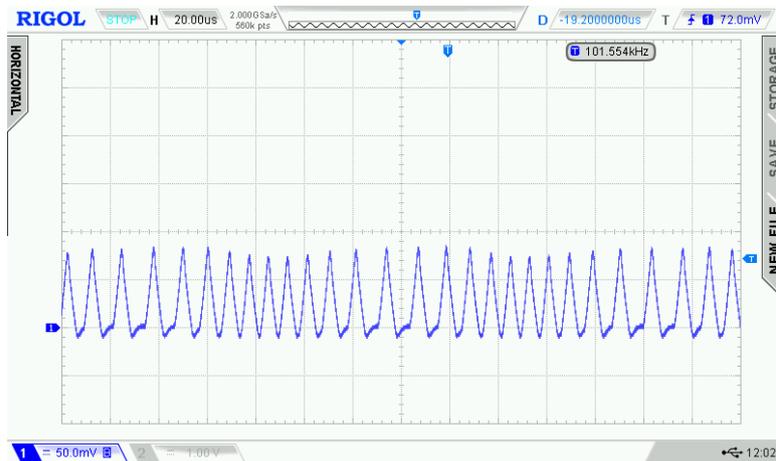
Figure 7. Experimental output voltage waveforms of boost SMPC in DCM: (a) for boost SMPC without SFM; (b) for boost SMPC with SFM only; (c) for boost SMPC with the hybrid-modulation-based technique. (Scale: 20mV/div; 20 μ s/div)



(a)



(b)



(c)

Figure 8. Experimental inductor current waveforms of boost SMPC in DCM: (a) for boost SMPC without SFM; (b) for boost SMPC with SFM only; (c) for boost SMPC with the hybrid-modulation-based technique. (Scale: 1.11A/div; 20 μ s/div)

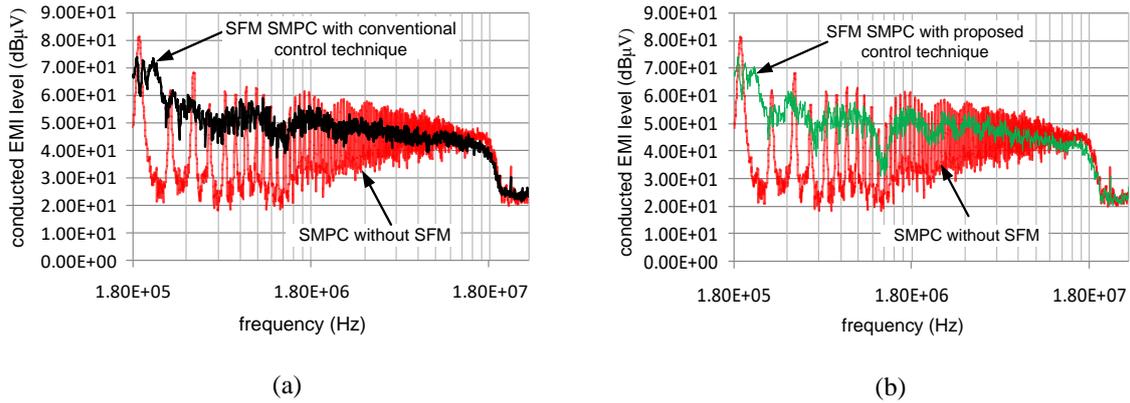


Figure 9. Experimental conducted EMI spectra of boost SMPC in DCM. Parameters: $f_m=9.5$ kHz, $f_{sw0}=100$ kHz, $\Delta f_{sw}=30$ kHz; $m(t)$ is sine; $V_{in}=12$ V. Note that the spectra were measured using a spectrum analyzer Agilent E4402B with RBW=9 kHz and self-made line impedance stabilization network (see Figure 10) in frequency range 180 kHz – 30 MHz

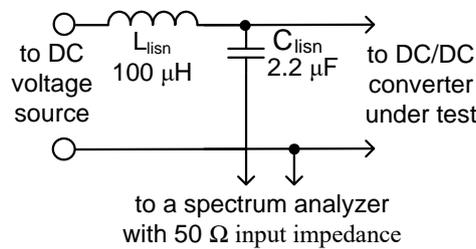


Figure 10. Self-made line impedance stabilization network (LISN) schematic diagram used in the experiments. Note: 1) the self-made LISN is not standard specific device; 2) the spectrum analyzer used in the measurements was connected to the LISN via coaxial cable with length of 9.5 cm and parasitic capacitance of 10.8 pF

Measurements of peak-to-peak output voltage, peak inductor current and conducted emissions levels for other values of Δf_{sw} , input voltages (8 V – 14 V) and output loads showed that the proposed control technique based on the hybrid modulation is very effective too. Measurements of efficiency of the boost SMPC showed that there is no any noticeable deteriorations of the efficiency when the proposed control technique is used. A comparison of the performance characteristics of SMPC with different control techniques is shown in Table 4.

Table 4. Comparison of the performance characteristics of SMPC with different control techniques

	Conventional SMPC without SFM	SMPC with SFM	SMPC with the proposed control technique based on hybrid modulation
Peak-to-peak output voltage	+ (the smallest)	- (the highest)	+ (slightly higher than for conventional SMPC without SFM)
Conducted emissions levels	- (the highest)	+ (appreciably lower than for conventional SMPC without SFM)	+ (appreciably lower than for conventional SMPC without SFM)
Peak power components currents	+ (the smallest)	- (the highest)	+ (slightly higher than for conventional SMPC without SFM)

4. CONCLUSION

The proposed control technique based on hybrid modulation scheme in which both switching frequency and duty ratio are simultaneously modulated by the same modulation signal is effective technique which can be applied to single-switch switch-mode power converters with classical topologies to reduce peak-to-peak output voltage and peak currents increased due to the use of conventional control scheme in which only switching frequency is modulated. Both computer simulations and experiments show that switching-frequency-modulated switch-mode power converter with the proposed control technique in comparison to that without switching frequency modulation has appreciably lower conducted electromagnetic interference levels, at the cost of slightly increased peak-to-peak output voltage and peak currents.

The proposed control technique based on the hybrid modulation has some limitations. Firstly, it is very useful to reduce peak-to-peak output voltage and peak currents significantly only when modulation frequency is higher than the open loop gain crossover frequency. For modulation frequencies lower than the crossover frequency this technique is much less effective. Secondly, it is very useful to reduce peak-to-peak output voltage significantly only when output capacitor with capacitive reactance much lower than equivalent series resistance is used (as in case of output electrolytic capacitor). The technique proposed is less effective to reduce peak-to-peak output voltage when output capacitor with capacitive reactance much higher than equivalent series resistance is used (as in case of output ceramic capacitor). However, peak currents can be reduced as effectively as in case when electrolytic capacitors are used at output of SMPC.

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